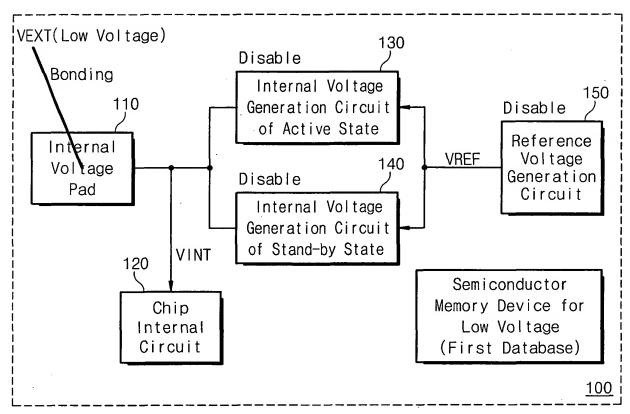
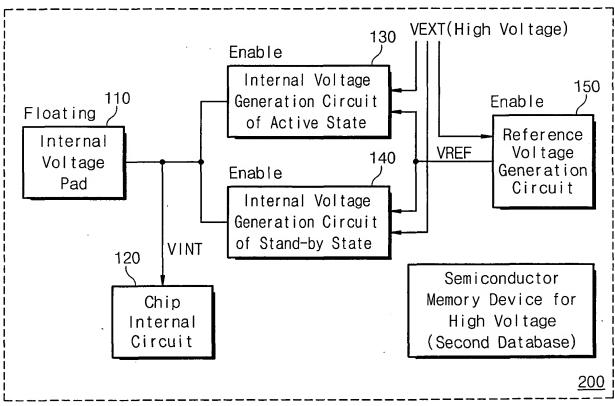
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SEMICONDUCTOR MEMORY DEVICE HAVING AN INTERNAL VOLTAGE GENERATION CIRCUIT FOR SELECTIVELY GENERATING
AN INTERNAL VOLTAGE ACCORDING TO AN EXTERNAL VOLTAGE LEVEL
Attorney Docket No. 4591-342/Client No. ID12015-US

Fig. 1

(PRIOR ART)





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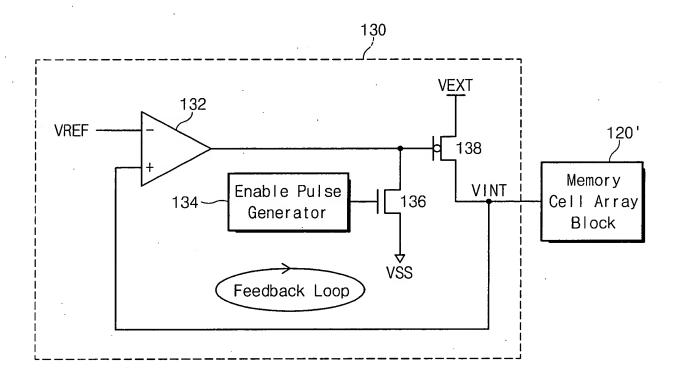
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Attorney Docket No. 4591-342/Client No. ID12015-US

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Fig. 2

(PRIOR ART)



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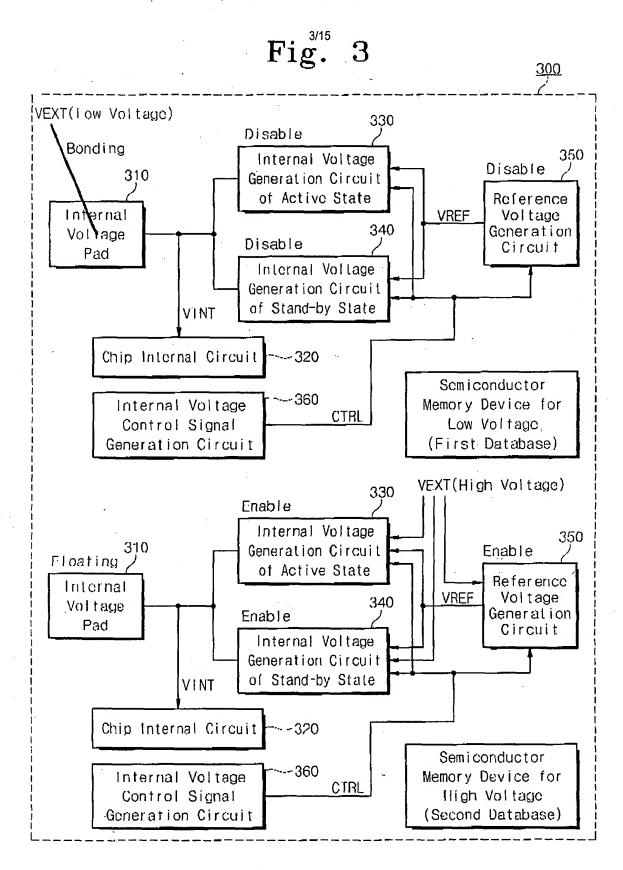
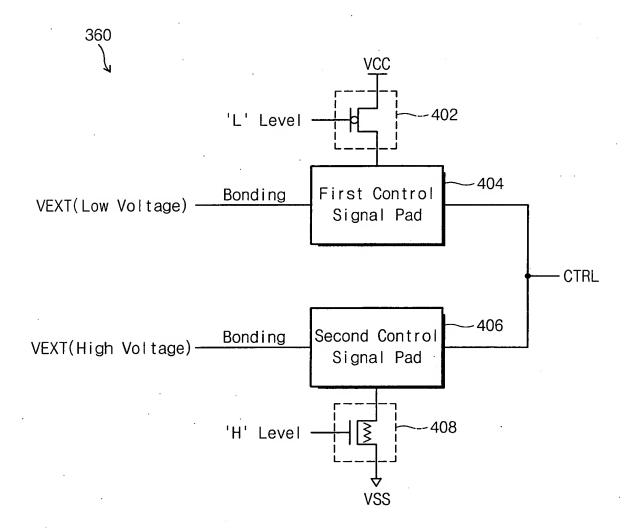
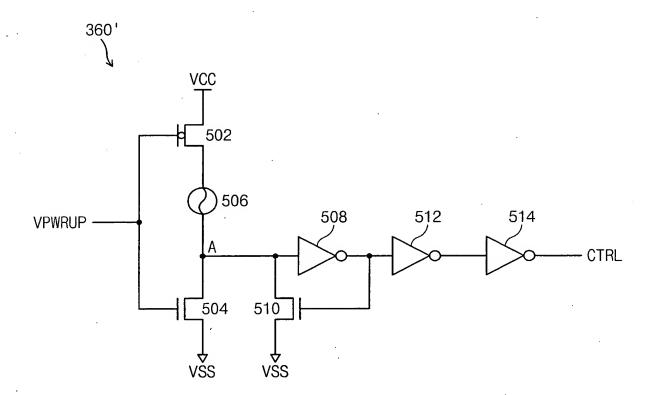


Fig. 4







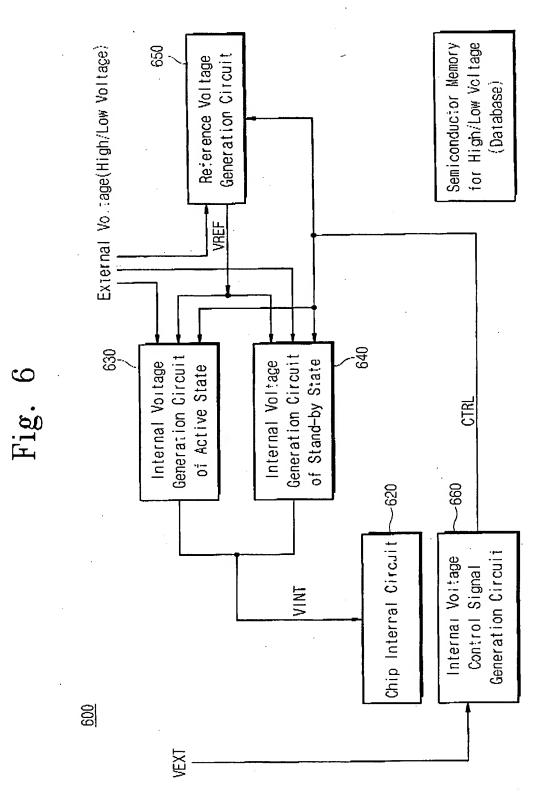
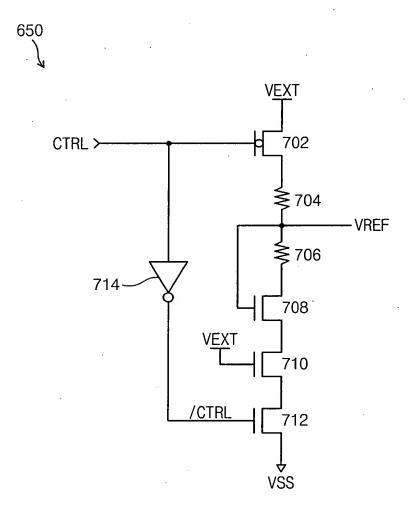
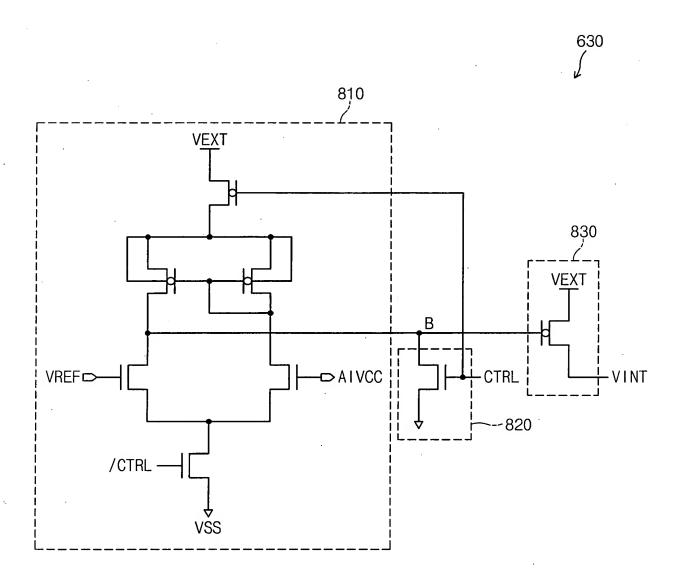


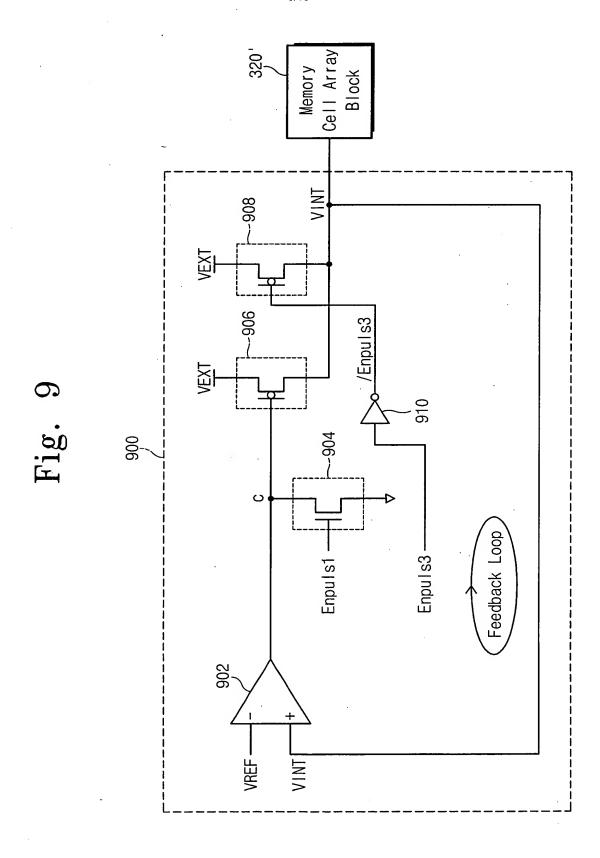
Fig. 7

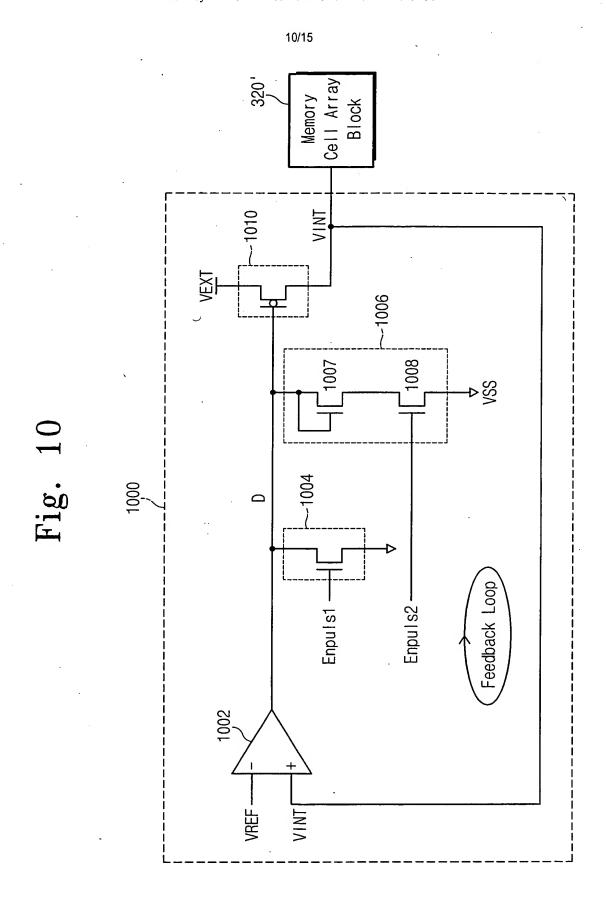


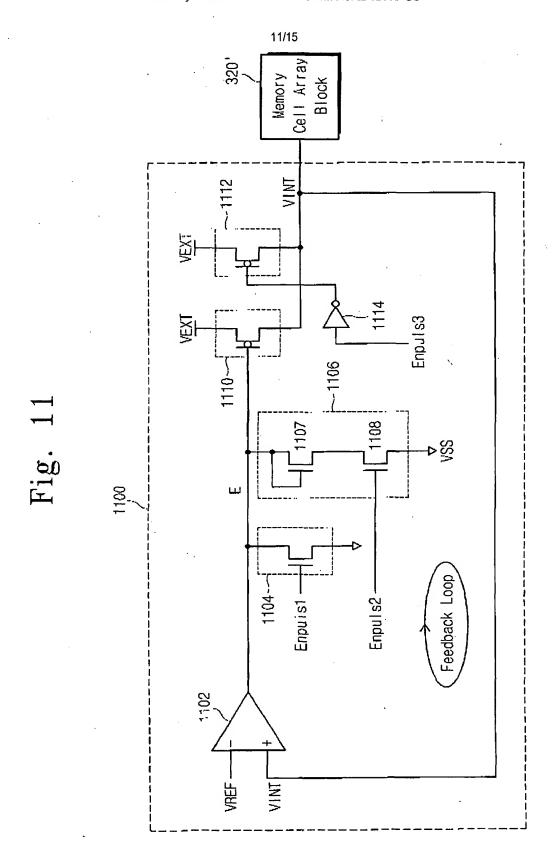
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Fig.



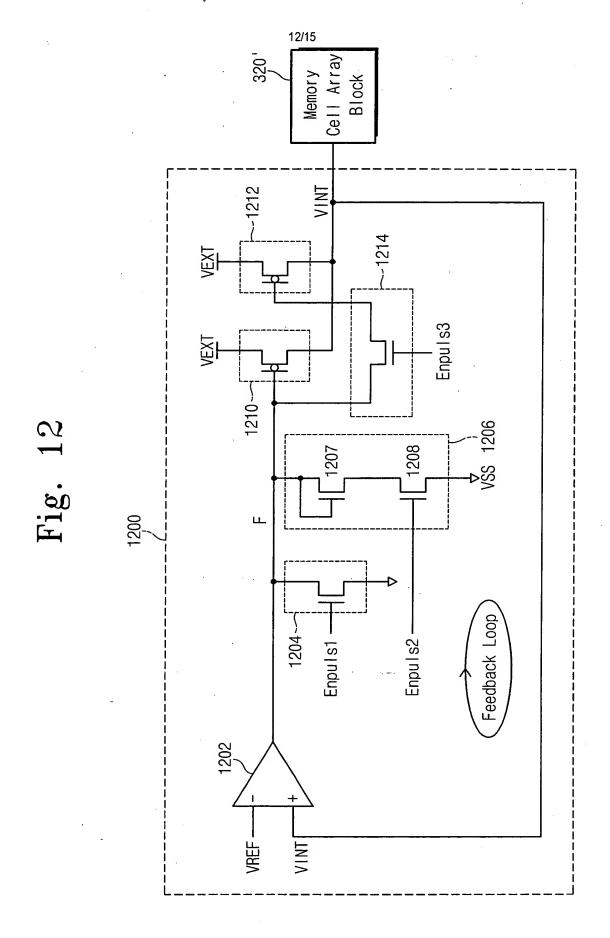


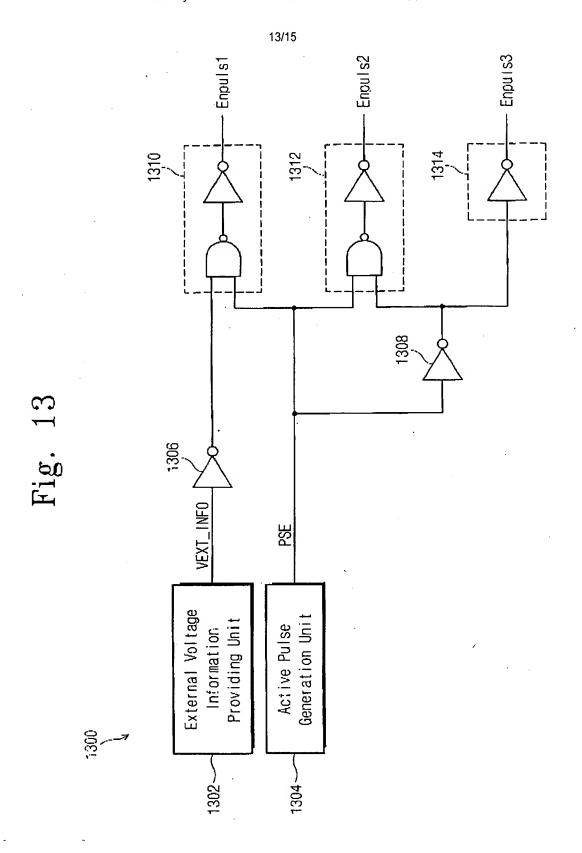


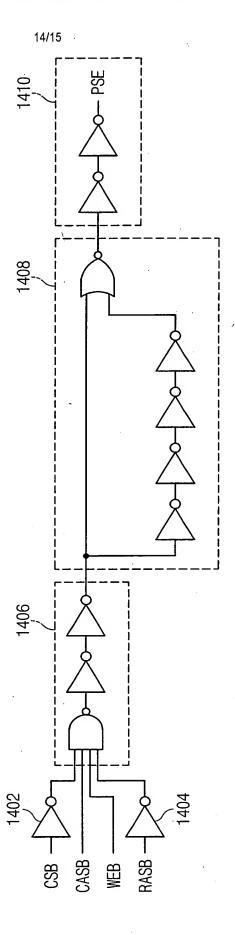


Kyu-Chan Lee, Sang-Jae Rhee, Jung-Yong Choi, Jong-Hyun Choi,
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Fig. 15

